



500.32647CC8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: A. SAKATA, et al.

Serial No.: 10/626,532

Filed: July 25, 20003

For: SEMICONDUCTOR INTEGRATED CIRCUITS WITH POWER
REDUCTION MECHANISM

STATUS LETTER AND REQUEST FOR OFFICIAL FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 22, 2003

Sir:

Applicants note that the office of the undersigned has not received the Official Filing Receipt for the above-identified application and applicants respectfully request the Official Filing Receipt.

An early response to this request is earnestly solicited.

Respectfully submitted,

Antonelli, Terry, Stout & Kraus, LLP

Gregory E. Montone
Registration No.: 28,141

GEM/rr